NATIONAL INSTITUTE OF TECHNOLOGY KARNATAKA, SURATHKAL
राष्ट्रीय प्रौद्योगिकी संस्थान कर्नाटक, सुरत्कल
P.O SRINIVASNAGAR, MANGALORE - 575025

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EC Call Letter List

PhD, 2023 - 24

The following applicants have been selected for written exam and/or interview for the department for the department of Electronics and Communication Engineering for PhD Programme. The applicants are requested to go through additional information provided in their Call letters.

#	Name	Reference Number	Branch/Specialisation
1	RENUKA B	PH2023EC0001	Electronics and Communication Engineering
2	Aditya Aron	PH2023EC0003	Electronics and Communication Engineering
3	PAVITHRA SHARANAPPA GUDADUR	PH2023EC0004	Electronics and Communication Engineering
4	SAI SUDHEER KOTTA	PH2023EC0005	Electronics and Communication Engineering
5	Anand Raj S N	PH2023EC0008	Electronics and Communication Engineering
6	Ambrish Upadhyay	PH2023EC0009	Electronics and Communication Engineering
7	PREETHA DSOUZA	PH2023EC0011	Electronics and Communication Engineering
8	Chaitra U R	PH2023EC0012	Electronics and Communication Engineering
9	Ashwini	PH2023EC0013	Electronics and Communication Engineering
10	ROHAN ROHIDAS NAIK	PH2023EC0014	Electronics and Communication Engineering
11	ANAND V KULKARNI	PH2023EC0015	Electronics and Communication Engineering

23/5/23

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EC Call Letter List
PhD, 2023 - 24

Head Of Department
Electronics and Communication Engineering

Dean Academics

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
NATIONAL INSTITUTE OF TECHNOLOGY KARNATAKA SURATHKAL
P.O. SRINIVASNAGAR, MANGALURU-575 025
Telephone: 0824-2473046, Website: www.ece.nitk.ac.in

Date: 24-05-2023

Shortlisted candidates for Ph.D. Programme–Written Aptitude Test / Interview for the year 2023-24

With reference to your application for admission to PhD programme in the department of Electronics and Communication Engineering, you are requested to appear for the Written test/Interview at NITK Surathkal. You should produce all the original records such as Date of Birth Certificate, GATE Score Card (if applicable), Degree Certificate and Marks Cards of all semesters (UG and PG programmes), SC/ST/OBC/EWS certificate (if applicable as per proforma), Person with disability certificate (if applicable), Sponsorship letter (if applicable), Conduct Certificate and valid photo identity card. Please keep a self-attested photocopy of all these certificates readily available at the time of interview.

Department	: Department of Electronics and Communication Engineering
Place of Reporting	: Department of Electronics and Communication Engineering, NITK Surathkal
Written Test Date and Time	: June 19, 2023, 9.00 AM at ECE Department
Announcement of shortlisted candidates for interview	: June 19, 2023, 12.00 PM
Interview Date and Time	: June 19, 2023, 2.00 PM Onwards, Meeting room, ECE Department June 20, 2023, 9.00 AM Onwards, Meeting room, ECE Department

NOTE:

1. Candidates should be prepared to appear for the written Aptitude Test before the interview. Fee Structure for PhD programme and Course syllabus are provided on Institute's website, i.e. www.nitk.ac.in. A copy of the syllabus for the aptitude test is given in a separate page.
2. Full-time/External Registrants - sponsored from Industry or other organizations including Educational Institutions, should have been serving in the sponsoring organisation for a period of at least 2 years after qualifying degree have to produce a letter from their employer stating that the candidate is deputed for Research Programme (Full time /External Registrant) in the Institute on **full salary** during the study period. The employer should indicate that the candidate will not be withdrawn midway before the completion of the course. (Sponsorship letter should be in the format provided in the Application Form).
3. Candidates who have not submitted marks of final examination along with application form shall produce the same at the time of admission if available.
4. Your candidature for this test is provisional & is subject to your fulfilling the educational qualifications & other criteria prescribed for the programme as mentioned in Information Brochure, failing which your candidature can be summarily rejected after verification/scrutiny at a later stage.
5. Please keep the Admit Card ready during the offline test and interview. You are responsible for safe custody of the Admit Card and in the event of any other person using this Admit Card, the responsibility lies on you to prove that you have not used the service of an impersonator.
6. Please note that no expenses shall be payable for appearing in the written test/Interview.

24/05/2023

Head of the Department

Mangalore - 575 025. KARNATAKA. INDIA
पी.ओ. श्रीनिवासनगर, मंगलूर - 575 025, कर्नाटक, भारत
National Institute of Technology Karnataka, Surathkal
Department of Electronics & Communication Engineering

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Date: 24-05-2023

Syllabus for PhD Programme Aptitude Test- June 2023

The Test paper has 2 Parts, Part-1 is compulsory, and Part -2 is stream specific modules. The candidate is supposed to attempt Module A or Module B or Module C from Part-2 depending on the domain in which a candidate wishes to pursue PhD. Part-1 has 15 multiple choice type questions, whereas, each module of Part-2 has 15 multiple choice type questions. Each correct answer carries 1 mark and wrong answer carries -0.25 marks.

Note: Total duration of Exam is one hour. Calculator is permitted.

Part-1:

Linear Algebra, Calculus, Differential and Difference equations. Numerical methods, Transforms, Linear circuits and networks, Electronic components and Devices, Analog Electronics, Digital Electronics, Signals and Systems, Linear and Digital Control Theory

Part-2:

Module-A (CEN Stream)

Electromagnetic Waves, Probability and Random Processes, Communication Theory, Communication Circuits, Transmission Lines, Wave Guides, Antennas, Microwave devices and Circuits, Data Communications, Communication Networks, Satellite Communication, Optical Communication, Fundamentals of Signal Processing.

Module-B (SPML stream)

Time domain analysis of discrete-time systems - Basic discrete time signals, discrete-time Fourier Series, Z Transform – definition and properties, Discrete-time Fourier Series and its properties, Properties and applications of DTFT. Relationship between time, Z and frequency domains, DFT fundamentals and Properties of DFT. FIR and IIR filters analysis and design, Fundamentals of image processing. Data structures, Linked list, stacks and queues.

Module-C (VLSI Design)

Linear and Digital ICs, Digital System Design, VLSI Technology, CMOS VLSI, Mixed Signal Design, HDL, Data converters, Microprocessors, Computer Architecture and organization, Logic Synthesis, DSP Architectures, Embedded Systems.